

L1_Cal Int/Comm Team Toolbox

July 25-29, 2005

Mon, Jul 25	<p>ADF→TAB (Mike/Cory) Establishing Sync. TAB→L2 (Mikolaj) Assisting in FIC ch0 issue. TAB→L3 (Mike/Lyn) Investigating BER software availability from Bowden/Zmuda. (Sabine) Unpacker work. ADF→TAB (Rahmi) Submitted parity test write-up. WIENER→EPICS (Savage/Adam) Developing GUI. Transition System (Jorge) Submitted test procedure write up. Review with Alan in progress. ORC (Charlotte) DC distribution in progress.</p>
Tues, Jul 26	<p>ADF→TAB (Mike/Cory) Identified two timing windows. TAB→L2 (James/Anderson) Resolved FIC EOFL (word) errors w/cap on FIC. Investigating GOFL (protocol) errors. TAB→L3 (Mike/Lyn) Bowden defining LINK test application capability on VRB for BER. (Sabine) Unpacker work. GAB→L2 (Mulhearn/Cory) No LED level trans. errors detected. WIENER→EPICS (Savage/Adam) Dev. GUI, hardware provided. TRANS_SYS (Jorge) Test Proc. Review sched. w/Alan for Wed. Resolving ACT FP spacing issue. ORC (Charlotte) DC distribution in progress.</p>
Wed, Jul 27	<p>ADF→TAB (Mike/Dan) Awaiting Dan's arrival to sync using hand pulser. TAB→L2 (James/Anderson) No update on GOFL errors TAB→L3 (Mike/Lyn) Bowden generating new application for EER/BER studies. (Sabine) Generating TAB test file for BER per Charlotte's grey code suggestion. GAB→TFW(Mike/Dan) Testing AND/OR terms today. TRANS_SYS (Jorge) Test Proc. Review sched. w/Alan for Wed. Resolved ACT FP spacing issue. ORC (Charlotte) DC distribution in progress.</p>
Thurs, Jul 28	<p>TAB (Mike/Cory) Completed timing of reg. Stratix chips (3 adj.). ADF→TAB (Mike/Dan/Sabine/Nayeem) Est. sync w/ bxing/tick. (Rahmi) Est. multiple card software. GAB→TFW(Mike/Dan) Verifying AND/OR terms w/ beam sync. TRANS_SYS (Jorge) Test Proc. Review submitted by Alan. Testing 10 ACT sets. Install 2 PPC.</p>
Fri, Jul 29	<p>GAB→TFW (Mike/Dan/Sabine/Cory) AND/OR terms synchronized. TAB→L2 (Mikolaj) 10M events/ no event errors. TAB→L3 (Mike/Sabine/Dan) Ready for // Prec. Readout. Will try from ADF(either memory/hand pulser) and then from TT today if possible. VRBC application firmware available to try from Mark Bowden. Scheduled for next week. ADF→TAB (Rahmi) Request time for sync parity tests. Sch next wk. TRAN SYS→(Alan) Installed PPC assemblies. Plans to install 10 ACTs, 40 PFCs, 30 LVDS 7.29-31. ORC (Charlotte/Dan) Dan is working on final ORC for ADF/Controls crate. Charlotte finishing DC for TAB-GAB/Readout crates.</p>